

Docket No.: MUH-12847

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : GEORG BRAUN ET AL.
Filed : CONCURRENTLY HEREWITH
Title : MEMORY MODULE AND MEMORY CONFIGURATION WITH
STUB-FREE SIGNAL LINES AND DISTRIBUTED CAPACITIVE
LOADS

INFORMATION DISCLOSURE STATEMENT

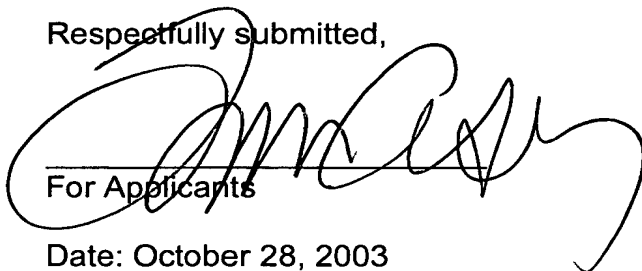
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent Application Publication No. 2001/0053069 A1 (Haba et al.), dated December 20, 2001.

Respectfully submitted,


For Applicants

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Date: October 28, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: MUH-12847 Appl. No.: <hr/> Applicant: GEORG BRAUN ET AL. <hr/> Filing Date: October 28, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	2001/0053069 A1	12/20/01	Haba et al.			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER				DATE CONSIDERED			